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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/660,887

09/12/2003

Mohamad El-Batal

LSI.78US01 (03-1076)

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24319

7590

02/11/2005

LSI LOGIC CORPORATION

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MS: D-106

MILPITAS, CA 95035

EXAMINER

VU, PHUONG T

ART UNIT

PAPER NUMBER

2841

DATE MAILED: 02/11/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No. 10/660,887	Applicant(s) EL-BATAL ET AL.	
	Examiner Phuong T. Vu	Art Unit 2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on RCE 1-24-05.
- 2a) ☐ This action is FINAL. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-8 and 11-18 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☐ Claim(s) _____ is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Claim Rejections - 35 USC § 112

1. The following is a quotation of the first paragraph of 35 U.S.C. 112:

The specification shall contain a written description of the invention, and of the manner and process of making and using it, in such full, clear, concise, and exact terms as to enable any person skilled in the art to which it pertains, or with which it is most nearly connected, to make and use the same and shall set forth the best mode contemplated by the inventor of carrying out his invention.

2. Claims 1-8, 11-18 are rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the written description requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to reasonably convey to one skilled in the relevant art that the inventor(s), at the time the application was filed, had possession of the claimed invention. The amended claim language of independent claims 1 and 11 requires "a first power supply connected to said first backplane, but not to said second backplane; a second power supply connected to said second backplane, but not to said first backplane;". This new limitation does not appear to be supported by the original specification and appears to conflict with the specification. It is noted in the specification (page 7, lines 27-31 and page 8, lines 2-4) that:

"The power supplies 220 and 222 may provide separate connectors that engage the backplanes 204 and 206. In some embodiments, the power supply connections may be routed through the interface board 212. While one power supply may be sufficient to operate the disk array 200, two or more power supplies may be used to provide backup and redundancy in the event of the power supply failure... two or more power supplies may be connected such that the failure of one power supply still allows the embodiment 200 to function while repair to the power supply is scheduled."

It appears from the specification that power supplies 220 and 222 respectively connect to backplanes 204 and 206. Alternatively, the power supplies are electrically connected to the interface board which is in turn connected to both the first backplane

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and the second backplane. In either case, it appears that when one of the power supplies fails, the other power supply provides power to the entire system shown as 200, including the first and second backplane and all the disks. Therefore, it is believed that in the first case, one power supply is required to be connected so as to supply power to both the first and second backplanes when the other power supply fails. In the second case, it appears that each of the first and second power supplies is connected to the first and second backplanes through the interface board.

Response to Arguments

3. Applicant's arguments filed 24 January 2005 have been fully considered. It appears that the newly added claim language seems to conflict with the specification. As noted above, it is supported in the specification that power supplies 220 and 222 each respectively connect to backplanes 204 and 206. Alternatively, the power supplies are electrically connected to the interface board which is in turn connected to both the first backplane and the second backplane. In either case, it appears that in the event of the failure of one the power supplies, each of the power supplies appear to be connected such that each power supply provides power to the entire system shown as 200, including the first and second backplane and all the disks. Therefore, it is believed that in the first case, one power supply is required to be connected so as to supply power to both the first and second backplanes when the other power supply fails. In the second case, it appears that each of the first and second power supplies is connected to the first and second backplanes through the interface board regardless of the occurrence of a power supply failure. In each case, it would seem that the first power

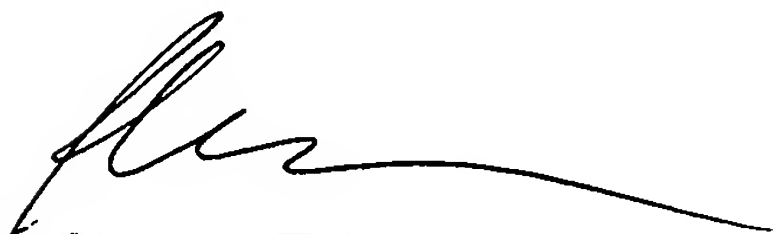
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supply is required to supply power to both the first and second backplanes when the second power supply fails and vice versa.

4. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Phuong T. Vu whose telephone number is (571) 272-2111. The examiner can normally be reached on Mon. & Tues., 7:30 AM - 4:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on (571) 272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Phuong T. Vu
Patent Examiner
Group 2841